Electronic Acknowledgement Receipt				
EFS ID:	5331312			
Application Number:	10750342			
International Application Number:				
Confirmation Number:	9469			
Title of Invention:	IDENTIFYING PROCESS AND TEMPERATURE OF SILICON CHIPS			
First Named Inventor/Applicant Name:	Jagrut Viliskumar Patel			
Customer Number:	23696			
Filer:	Jiayu Xu/Christine Molla			
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Attorney Docket Number:	030439			
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3	3 NPL Documents	030439WO_2005-04-06_ISR.pdf	244570	no	2
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4	4 NPL Documents	030439WO_2007-03-06_IPRP. pdf	777180	no	5
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5 NPL Documents	030439WO_2006-05-24_WO. pdf	703228	no	5	
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6 Fee Worksheet (PTO-875)	fee-info.pdf	32173	no	2	
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### National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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